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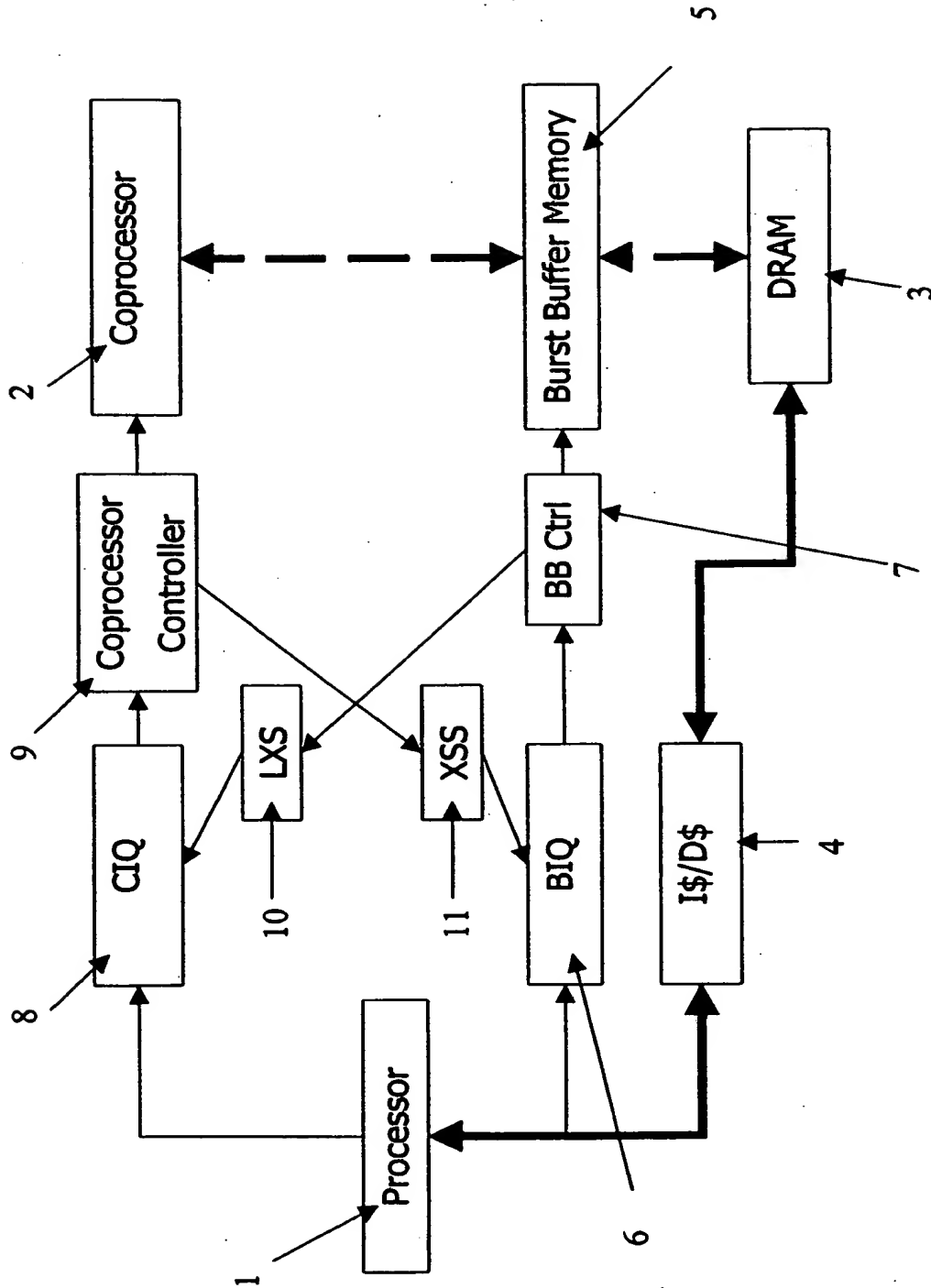


Figure 1

FIGURE 1



Figure 2

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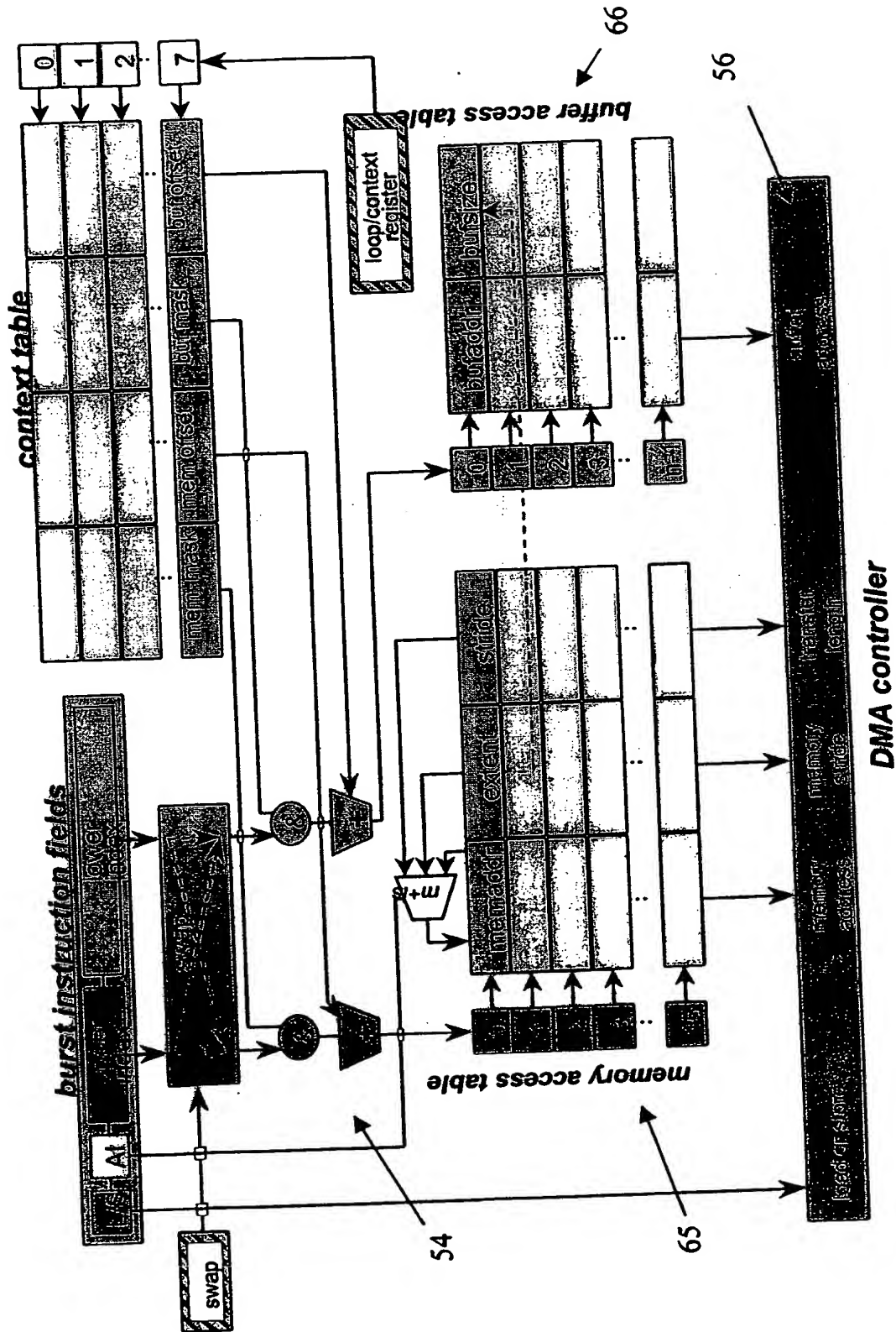


Figure 3

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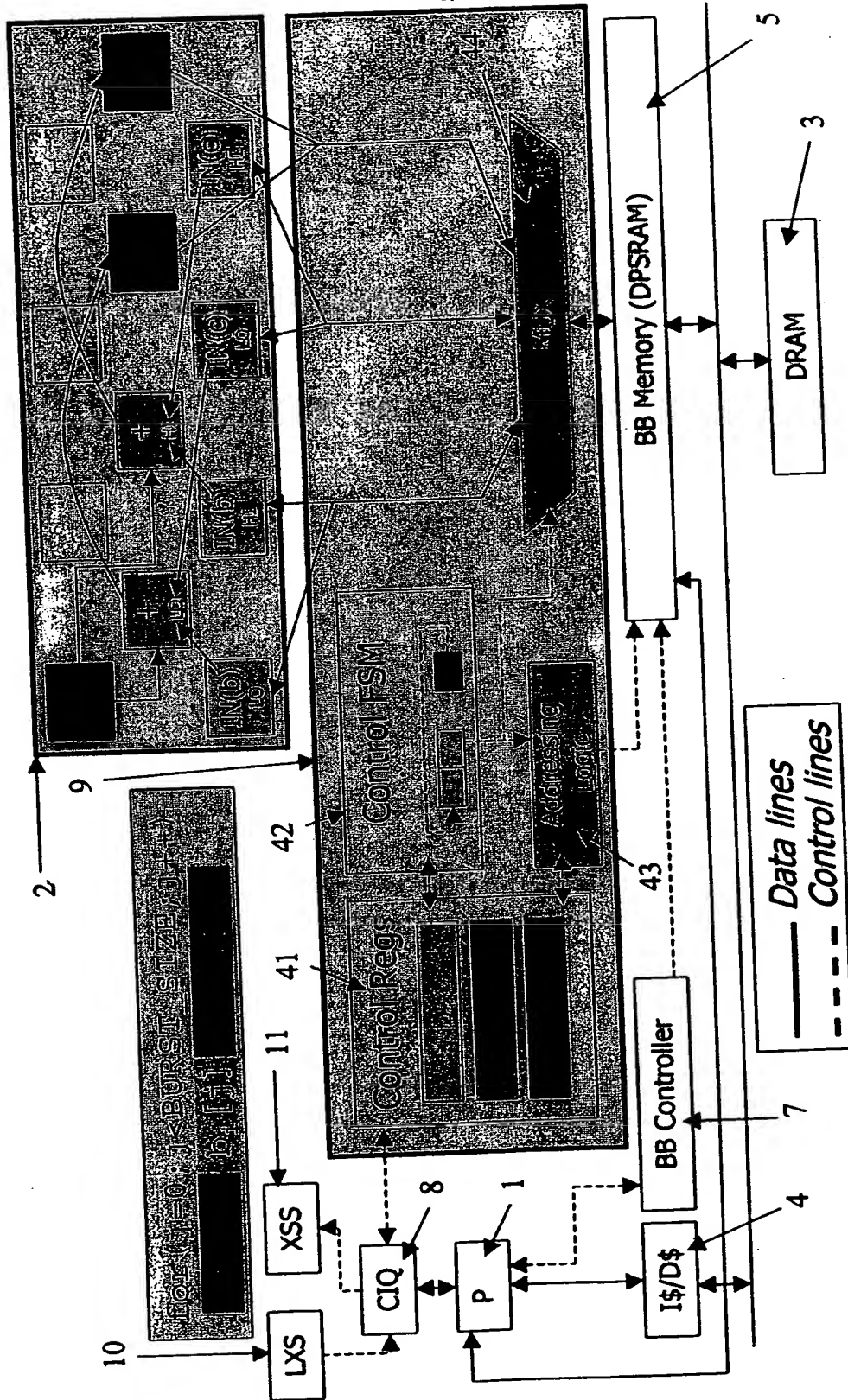


Figure 4

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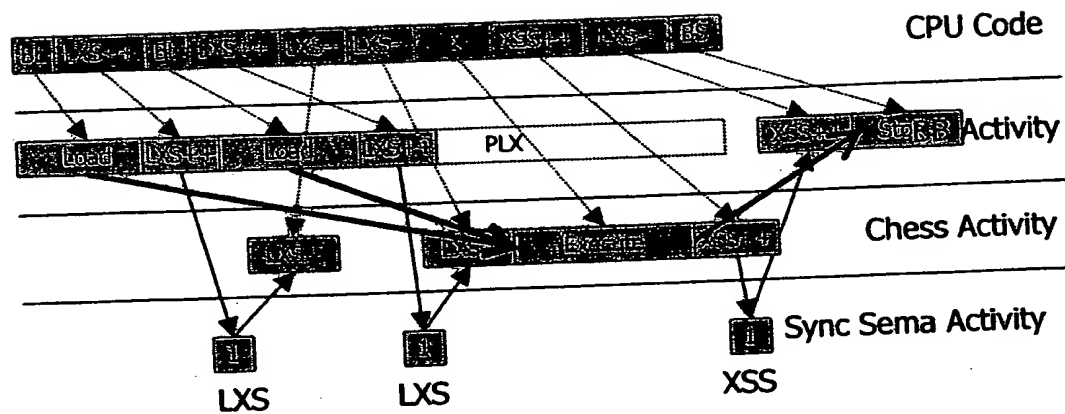


Figure 5

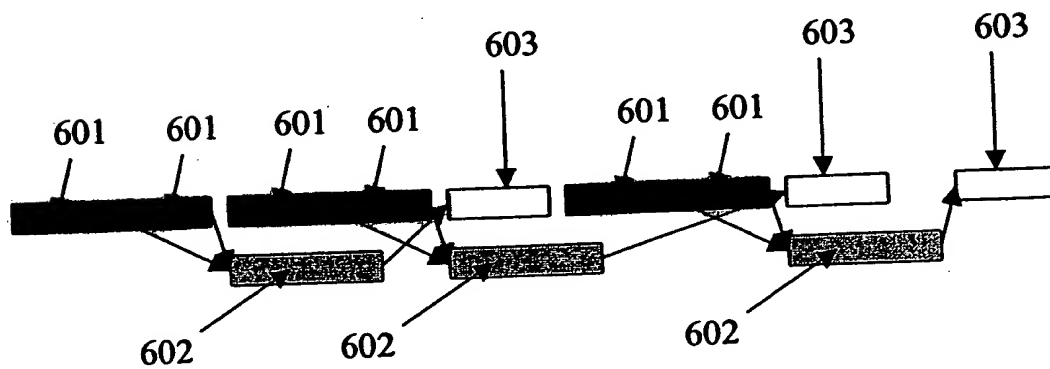
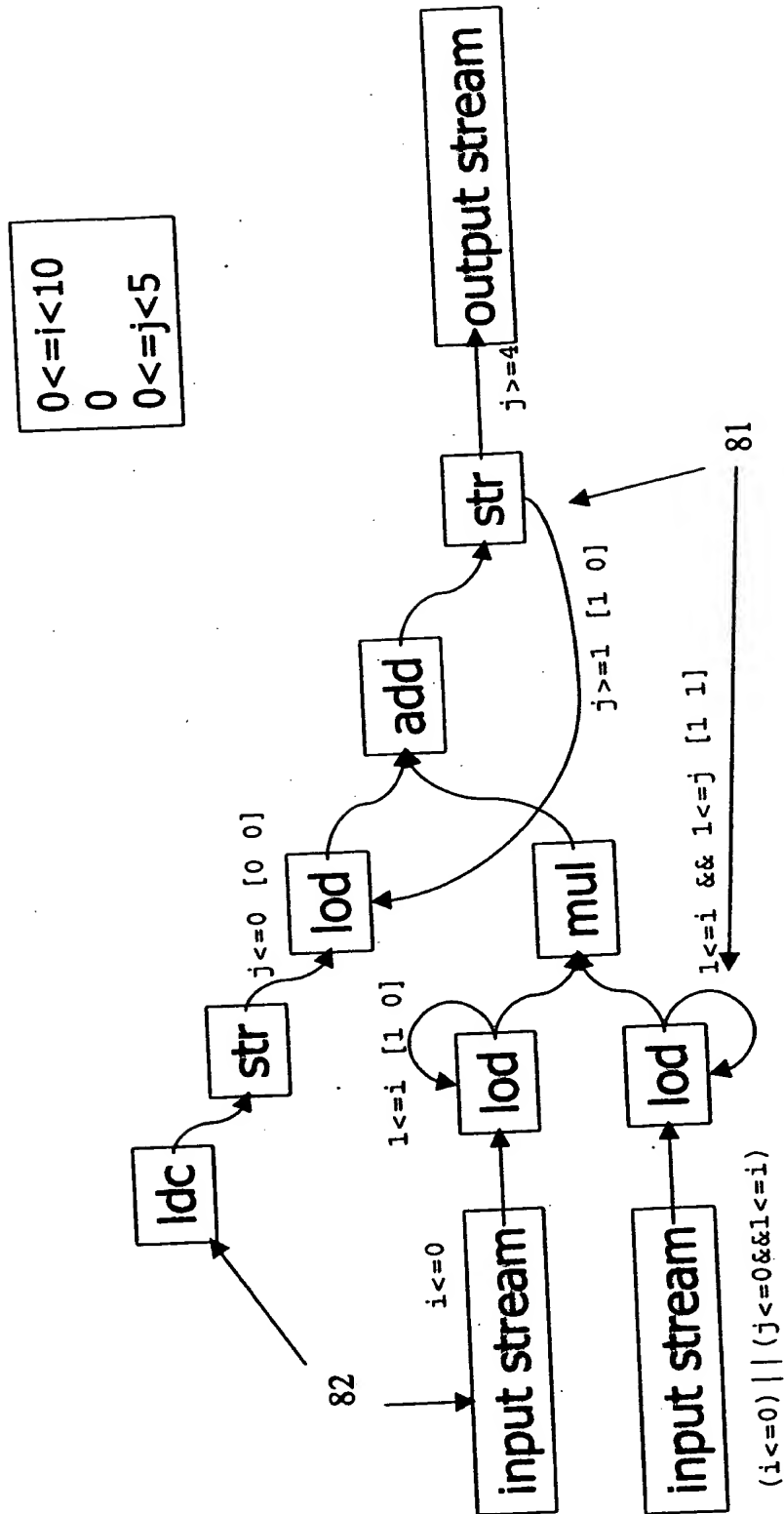


Figure 6

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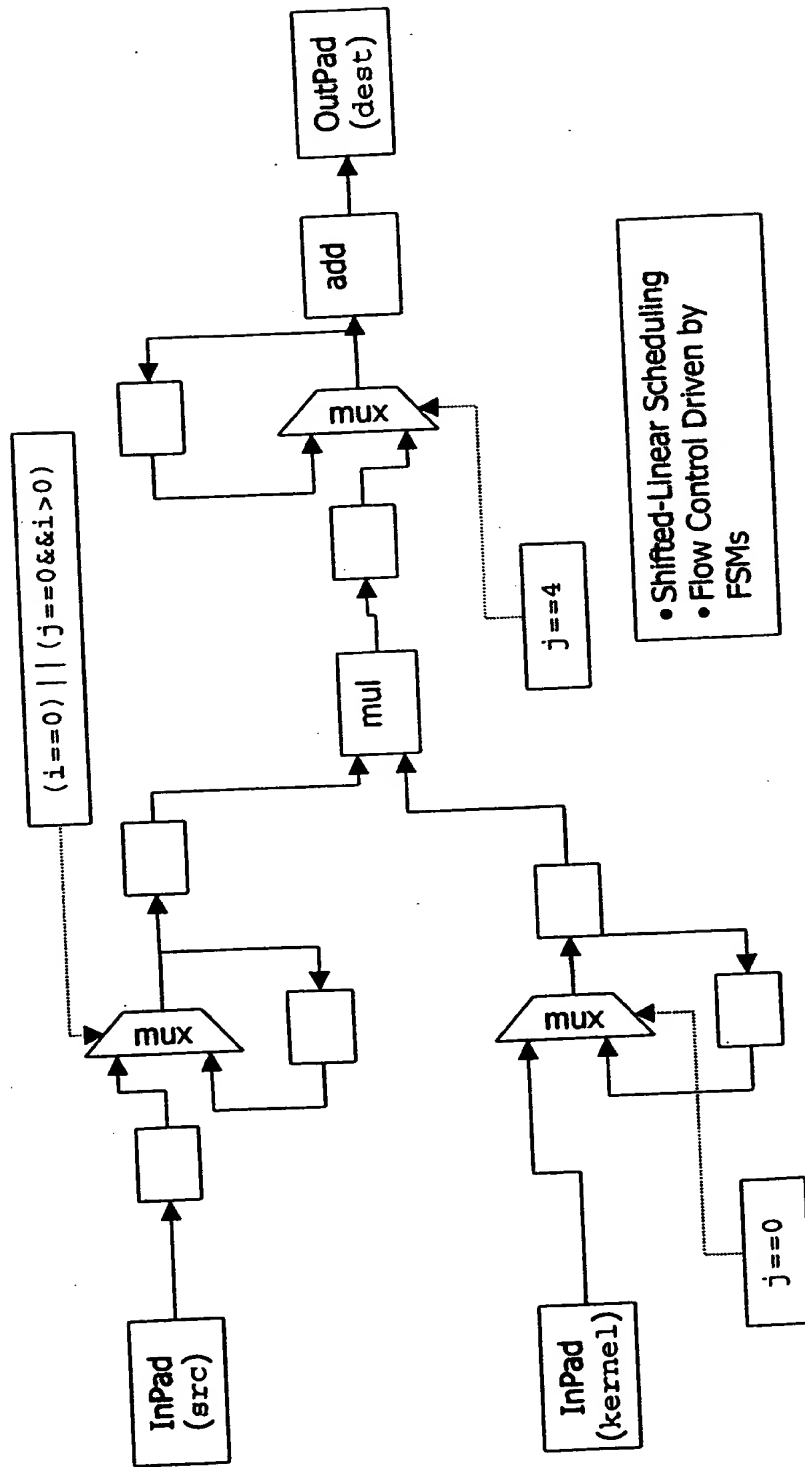


Figure 8

- Shifted-Linear Scheduling
- Flow Control Driven by FSMs

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5x5 Convolution (32-bit version):

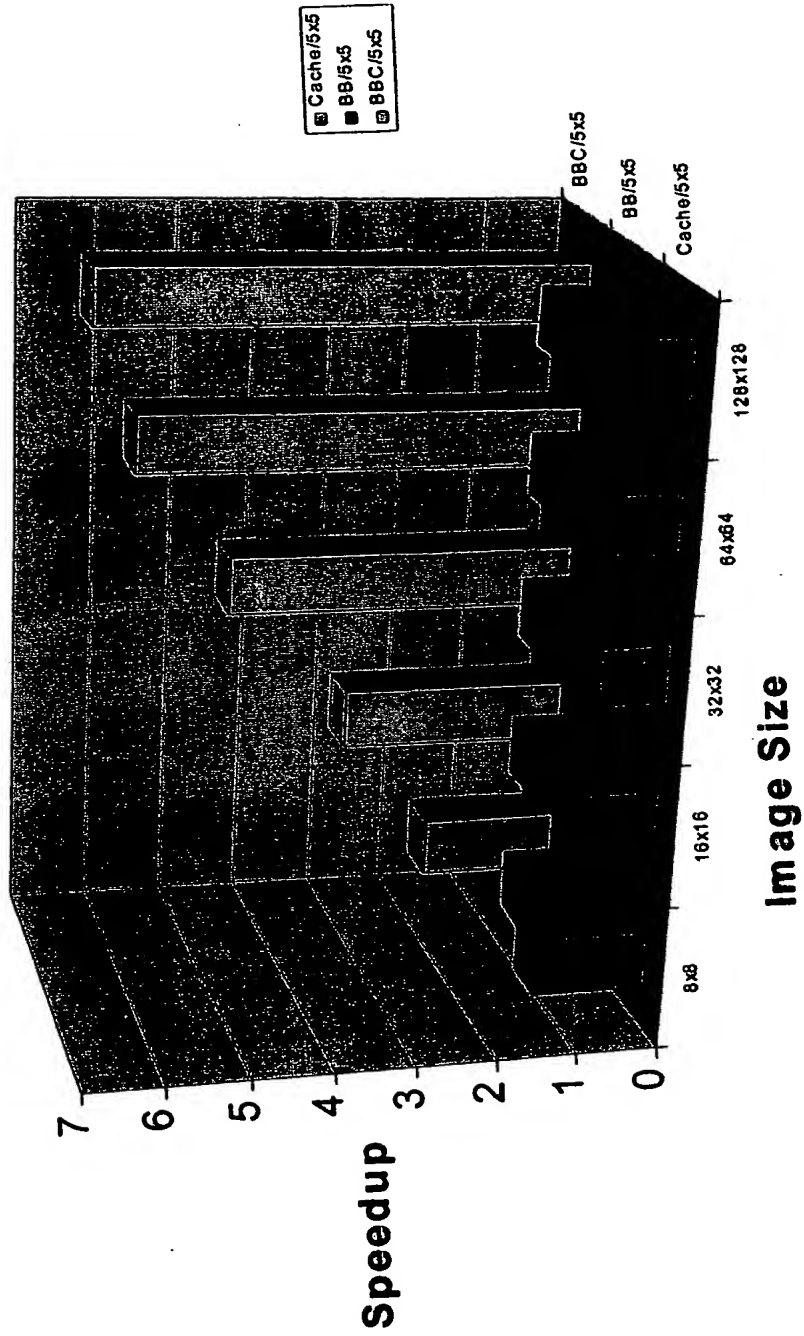


Figure 9

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5x5 Convolution (8-bit version):

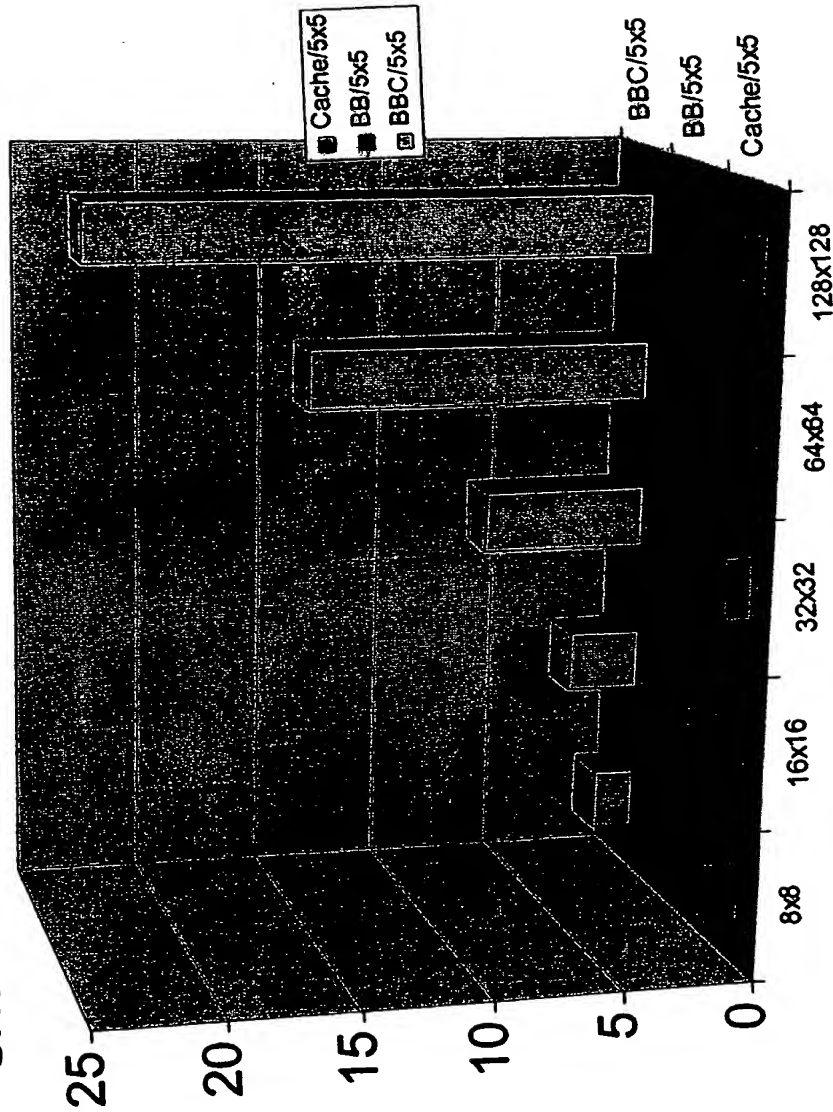


Figure 10



Figure 11B

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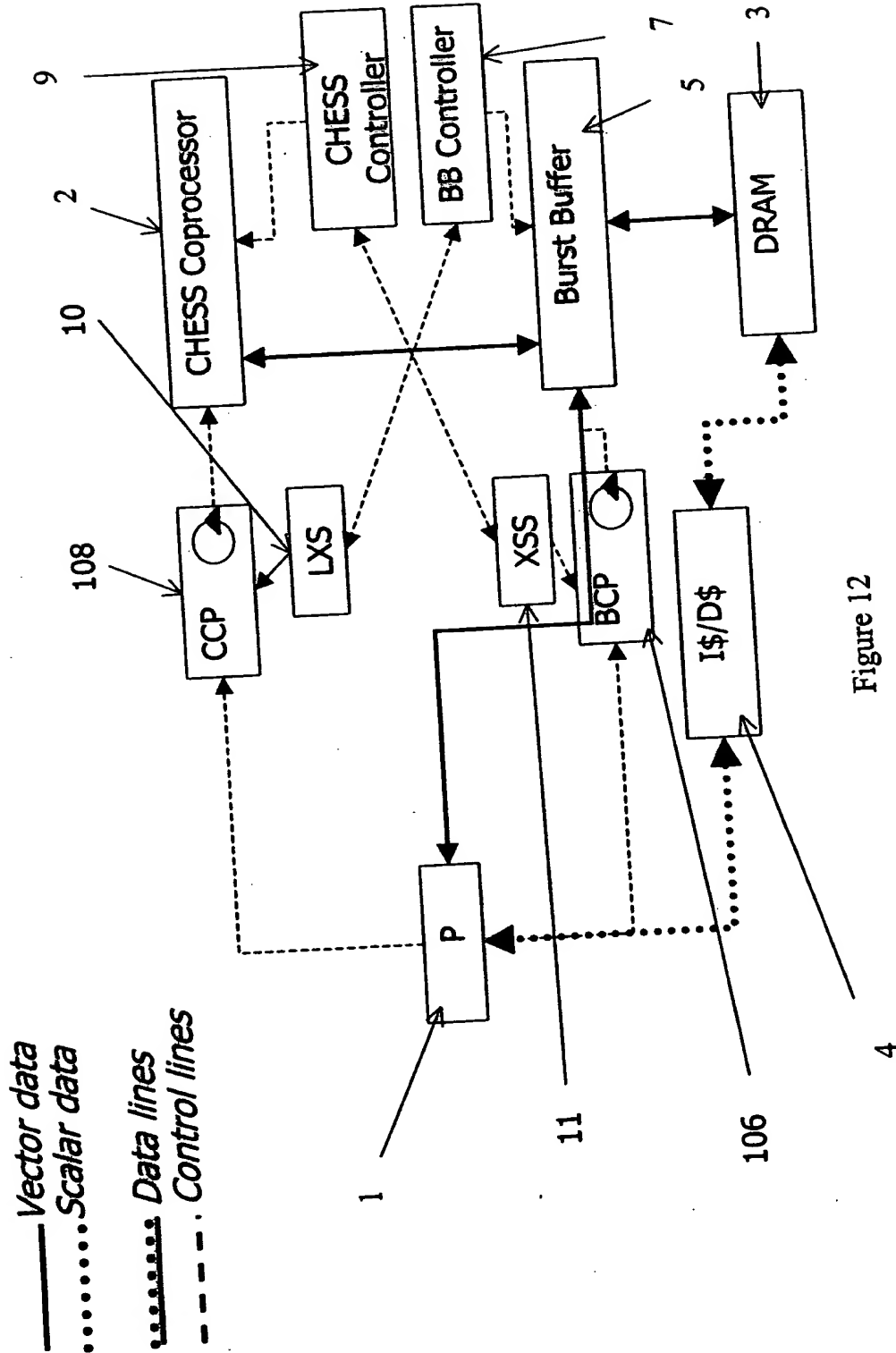


Figure 12

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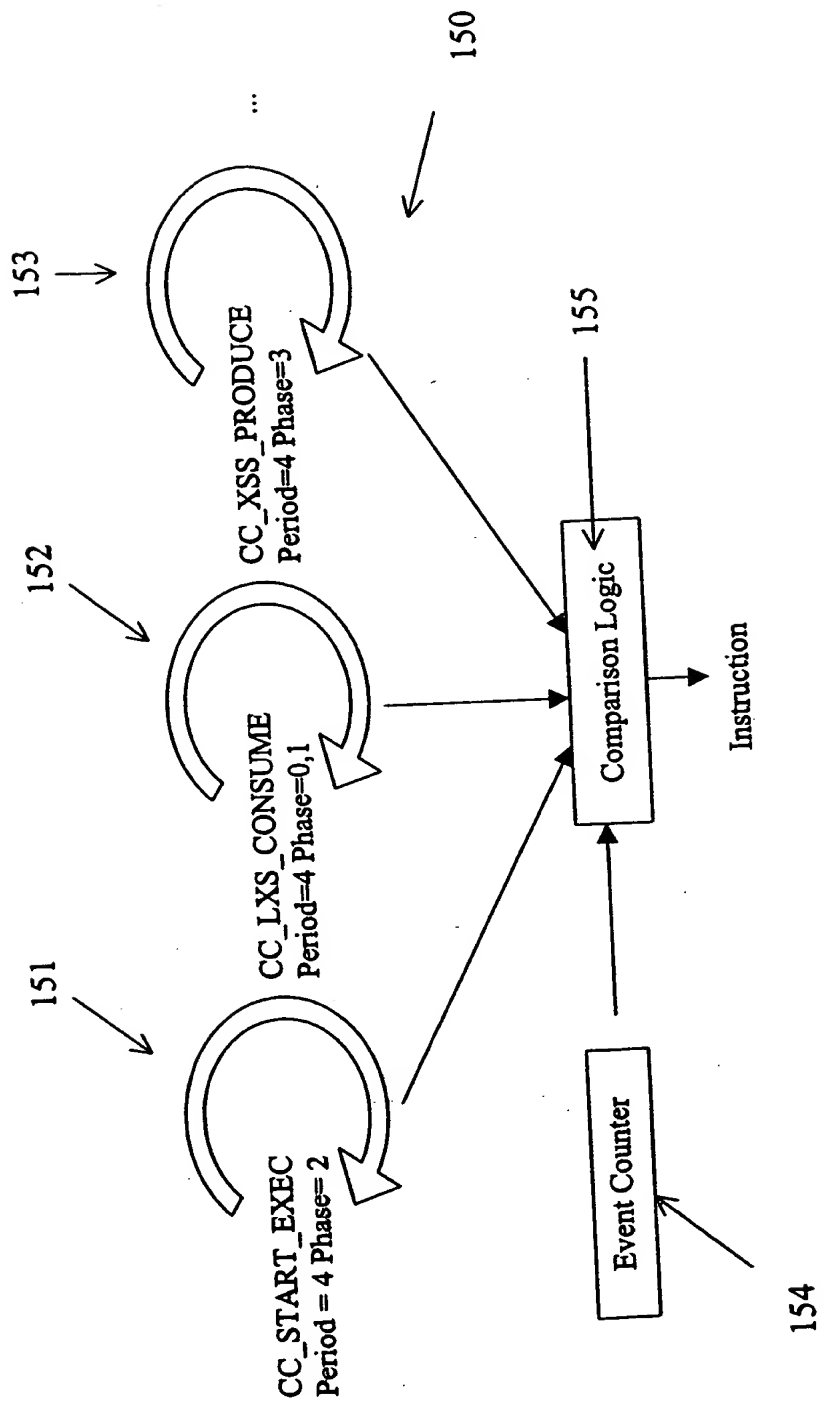


Figure 13